Profile

Name of the Faculty	J. Mrudula		
Designation	ASSOCIATE PROFESSOR		
Department	ECE		
Area of Interest	DIGITAL COMMUNICATIONS		
Subjects Taught	EDC, ECAD, EMTL, AC, DC, STLD,		
	LICA, MWE		
JNTUH Registration	09150402-163847		
Id			
College Staff Code	SC1321		
Official Mail	Mrudulasandesh.ece@gcet.edu.in		



Educational Qualifications:

S.		Degree	Specialization	University/College	Year
No.					
1	1	M.E	Industrial	M. S. University, Baroda	2000
			Electronics	tronics	
2		B.Tech	ECE	S. V. University/K. S. R.	1998
				M. College of	
				Engineering, Kadapa	
3		Intermediate	MPC	Board of Intermediate	1994
				Education/Nagarjuna	
				Junior College, Kadpa	

4	SSC	-	Board of Secondary/	
			Education/ Nirmala	
		English Medium High		
			School	

Paper Publications:

S. No.	Publication details			
1	BER due to Intra-Band Crossta	sstalk in Optical Cross Connects-		
_ A		National Conference on Latest Trends in Signal Processing, VLSI		
	and Embedded Systems-June-	2014-, National		
2	EYE DIAGRAM as an Diagnost	ic Tool for BER Analysis in High Speed		
	Serial Links- A Review-			
	I nternational Journal of Adva	nces in Engineering & Technology (I J AE		
	T)-May-2014-International	T)-May-2014-International		
3	Frequency Synthesizer for Satellite data with reduced Clock jitter			
	National Conference on Circuits Signals and Systems- 22-24, January			
	2015- National			
4	Parallel Prefix Tree 32-Bit Co	mparator And Adder By Using Scalable		
1	Digital CMOS, International Jo	urnal of Advanced Research in		
	Electronics and Communication	nication Engineering (IJARECE) Volume 3, Issue		
	10, October 2014			
5	Performance analysis of Propagation Models of Wi-MAX in urban,			
	suburban, International Journal of Advance Electrical and Electronics			
	Engineering (IJAEEE), Volume-3 Issue-3, 2014			
6	Design of Intelligent Solar Tracker Robot for			
	Surveillance, International Jou	al Journal of Advanced Research in Electrical,		

	Electronics and Instrumentation Engineering			
	(An ISO 3297: 2007 Certified Organization)			
	Vol. 2, Issue 10, October 2013			
7	Viterbi Decoder Based on Speed Optimization of a FPGA, International			
	Journal of Scientific Engineering and Technology Research,			
	Vol.03,Issue.27 September-2014,			
8	Modelling and Optimization Analysis of HIT-CBTSSe Tandem Solar Cell,			
	2nd International Conference on Micro/Nanoelectronic Devices,			
	Circuits and Systems, 29th to 31st January, 2022			
9	Minimum Mean Square error and Zero forcing receivers models			
	implementation for massive MIMO using MATLAB			
	Design Engineering, Dec 2021, International			

Books/Book Chapters Published:

S. No.	Publication details	
		y.

Experience:

Teaching	18 years	5 months	
Industry	-		
Research	-		
Total Experience	18 years 5 months		